



PCBA ICT project

Paul Deschenes

to: Edwin Koh, HuaZhen Luo, Jaaffar
Ismail, Matt Brule

15.01.2019 05:37

Cc: Dave Reed

Hi Edwin,

The first thing I would like to show you is where the files are located in SAP to make sure you and Luo have access to them.

In SAP use CV04N to find documents

- CV02N - Change Document
- CV04N - Find Document
- 7023 - Vendor Defects - Modified Insp. Rslt (7023)

Then Type in the board number

look for the last TEF File

		SWA	210000355	07 000	ASSY, PCB 2489 TUV PERSONALITY	CONCEPT	
A		TEF	210000355	00 000	TEST ENG. FILE, 2489 TUV PERSONALITY RHS	ENG_ALPHA_REL	
		TEF	210000355	01 000	TEST ENG. FILE, 2489 TUV PERSONALITY RHS	RELEASE GENERAL	
C		TEF	210000355	02 000	TEST ENG. FILE, 2489 TUV PERSONALITY RHS	RELEASE GENERAL	
D		TEF	210000355	03 000	TEST ENG. FILE, 2489 TUV PERSONALITY RHS	RELEASE GENERAL	
		TEF	210000355	04 000	TEST ENG. FILE, 2489 TUV PERSONALITY RHS	ABANDON	
F		TEF	210000355	05 000	TEST ENG. FILE, 2489 TUV PERSONALITY RHS	RELEASE GENERAL	
		TEF	210000355	06 000	TEST ENG. FILE, 2489 TUV PERSONALITY RHS	CONCEPT	
		TEF	210000355	07 000	TEST ENG. FILE, 2489 TUV PERSONALITY RHS	CONCEPT	

There you will find a zip folder with all the files necessary for the fixture

Document

Document Part Version

◇ Deletion Flag ◇ Document Structure ◇ CAD Indicator

Document Data Descriptions Object Links Originals

Document Data

Description

Document Status ☒ Not Released

CM Relevant

User Brian Alexander

Lab/Office Electircal Design Milford

Change Number Valid From

Authorization Group

Originals

Appl.	Application	Storage Cat.	...	...	File Name
• ZIP	WINZIP	ZPLM			210000355-H_TE.zip



in this ZIP folder you will find the assembly, fabrication, schematic, BOM and all the gerber files

the only other files that the programmer will need is if the board has any parts that need to be programmed the programming files will need to be sent.

Just a suggestion, you should add opens express to any chips they feel don't have good access but also to all surface mounted connectors like the large CPU2000 connector.

As for the board schedule, I asked Rich Leourneau for the list with time line

This is what he sent me

	A	B	C
1	Task Name	Duration	Start
2	Altera/Intel obsolescence replacement project	1804 days	Mon 5/8/17
3	Altera Obsolescence Project Plan	3 wks	Mon 5/8/17
4			
5	FPGA Implementation	966 days	Mon 5/29/17
6	Ziad El-Massih	725 days	Mon 5/29/17
7	Classic TUV Detector PCB: 210000355	505 days	Mon 5/29/17
8	2475 FLR PCB: 210000397: PLD 332000586	195 days	Mon 9/10/17
9	2998 PCB: 210000404: PLD 332000586	150 days	Mon 3/18/18
10	Acquity/Classic ELSD PCB: 210000408	162 days	Thu 7/25/18
11			
12	Gene Tancrell	966 days	Mon 5/29/17
13	Design Phase: TUV PCB:210000414: PLD 332000586	510 days	Mon 5/29/17
14	2414 RI PCB:210000331/20: PLD 332000530	153 days	Mon 4/15/18
15	Sample organizer Elevator PCB: 210000420: PLD332000530	153 days	Thu 10/17/18
16	PCM PCB: 210000456: PLD 332000530	205 days	Tue 4/21/20
17	TCM II PCB: 210000460: PLD 332000530	195 days	Tue 5/12/20
18			
19	Bryan Deguire	899 days	Mon 5/29/17
20	Sample/HDX Manage, 2200 PCB: 210000323: PLD 332000512	500 days	Mon 5/29/17
21	1525/1515 Binary High Gradient Pump, PCB: 21000057520: PLD 332000530	153 days	Mon 4/1/19
22	Acquity Column manager PCB: 210000389: PLD 332000512	153 days	Thu 10/3/19
23	515 PCB design: 210000562 FLEX 8000 FPGA: PLD 332000628	153 days	Tue 4/7/20
24			
25	CPLD Implementation	696 days	Fri 7/13/18
26	MicroChip ATF1508ASV-15AU100 Functional Compatible only	696 days	Fri 7/13/18
27	CPU2K: 210000520/691:PLD 332000422: CPLD, EPM3128ATC100-10N	181 days	Fri 7/13/18
28	Alliance Fluidic PCB: 210000416: PLD 332000422: CPLD EPM3128ATC100-10N	180 days	Fri 9/14/18
29	Trizaic Interface PCB: 210000465: PLD 332000422: CPLD EPM3128ATC100-10N	166 days	Thu 3/12/20
30	Trizaic Phase 1 interface PCB: 210000497: PLD 332000422: CPLD EPM3128ATC100-10N	166 days	Fri 5/15/20
31	Tile Interface PCB: 210000364: PLD 332000422: CPLD EPM3128ATC100-10N	166 days	Fri 7/24/20
32			
33	MicroChip ATF1508AS-10QU100: FFF compatible	166 days	Fri 10/12/18
34	TUV Pre-Amp PCB: 210000398/20: PLD 332000512: CPLD EPM7128SQC100-15FN	166 days	Fri 10/12/18
35			
36	MicroChip ATF1504AS-JU44	471 days	Fri 11/23/18
37	Alliance H/C PCB design: 210000444: PLD 332000741: CPLD EPM7064SLC44-10N	146 days	Fri 11/23/18
38	ELSD Pre-amp PCB: 210000814: PLD 332000741: CPLD EPM7064SLC44-10N	166 days	Fri 1/24/20
39	2414 RI Pre-Amp: 210000399: PLD 332000741: CPLD EPM7064SLC44-10N	166 days	Fri 1/24/20
40			
41	INTEL MAXIM5	171 days	Fri 10/26/18

42	Alliance CPU PCB: 210000411/966: PLD 332000746: CPLD EPM3064ATC100-4N	171 days	Fri 10/26/1
43			
44	INTEL MAXIM10	166 days	Fri 7/24/20
45	Vista Pump PCB: 210000422: PLD 332000602: CPLD EPM3512AQC208-10N	166 days	Fri 7/24/20
46	Triton Pump PCB: 210000339: PLD 332000602: CPLD EPM3512AQC208-10N	166 days	Fri 7/24/20
47	Prep pump 2545 PCB: 210000365: PLD 332000602: CPLD EPM3512AQC208-10N	166 days	Fri 7/24/20
48	Acquity Binary Pump PCB: 210000308: PLD 332000602: CPLD EPM3512AQC208-10N	166 days	Fri 7/24/20
49			
50	FPGA obsolescence Dec 2018 (Cyclone family, EP1C4F324C6N)	849 days	Mon 1/4/21
51	QSM personality PCB: 210000345: PLD 332000754 Cyclone 1 EP1C4F324C6N	223 days	Mon 1/4/21
52	CM-A: 210000478	180 days	Mon 1/4/21
53	SFC: 210000526	223 days	Mon 9/13/2
54	CM30S: 210000865	223 days	Mon 9/13/2
55	AcqPDA: 210000415	223 days	Thu 7/21/2
56	AcqRI: 210000699	223 days	Thu 7/21/2
57	CPSM: 210000367	223 days	Tue 5/30/2
58	CPSM2: 210000903	223 days	Tue 5/30/2
59			

Immediate projects in the works now:

The 210000414 project we will be taking care of that one here in Milford, I already have the funds to move forward.

The 210000323 board is made at CEI Dave Reed has already contacted them and they are looking forward to getting a quote. I know the Engineer is making a small change to this CAD and there will be a new revision but you should be able to get a quote with what is available then send the new files when released to build the fixture and program.

The 210000355 board is also close to ready to move forward. I know the engineer is looking to make one more small change to the CAD this will also be a new revision, but you should be able to move forward with the files available just to get a quote. This board has a new chip using a new programming platform the programmer will need to be aware of.

I am always available if you wish to have a conference call to clarify any of the details.

Regards
Paul



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