

A



C

D

F

Keypad Connector

323000321

KEYPAD INTERFACE

PRFRLDAT(15:8) <>>

LEDSTRB >>

ID TEN (10)

_IDENBIT >>

PRFRLDAT(15:8) <>>

_IRQ7 >>

MODCK >>

_BEEPER

SOFTWARE JUMPERS

WATERS

34 MAPLE STREET
MILFORD, MA 01757 U.S.A.

LAST UPDATE
11/21/2000 11:29

USER
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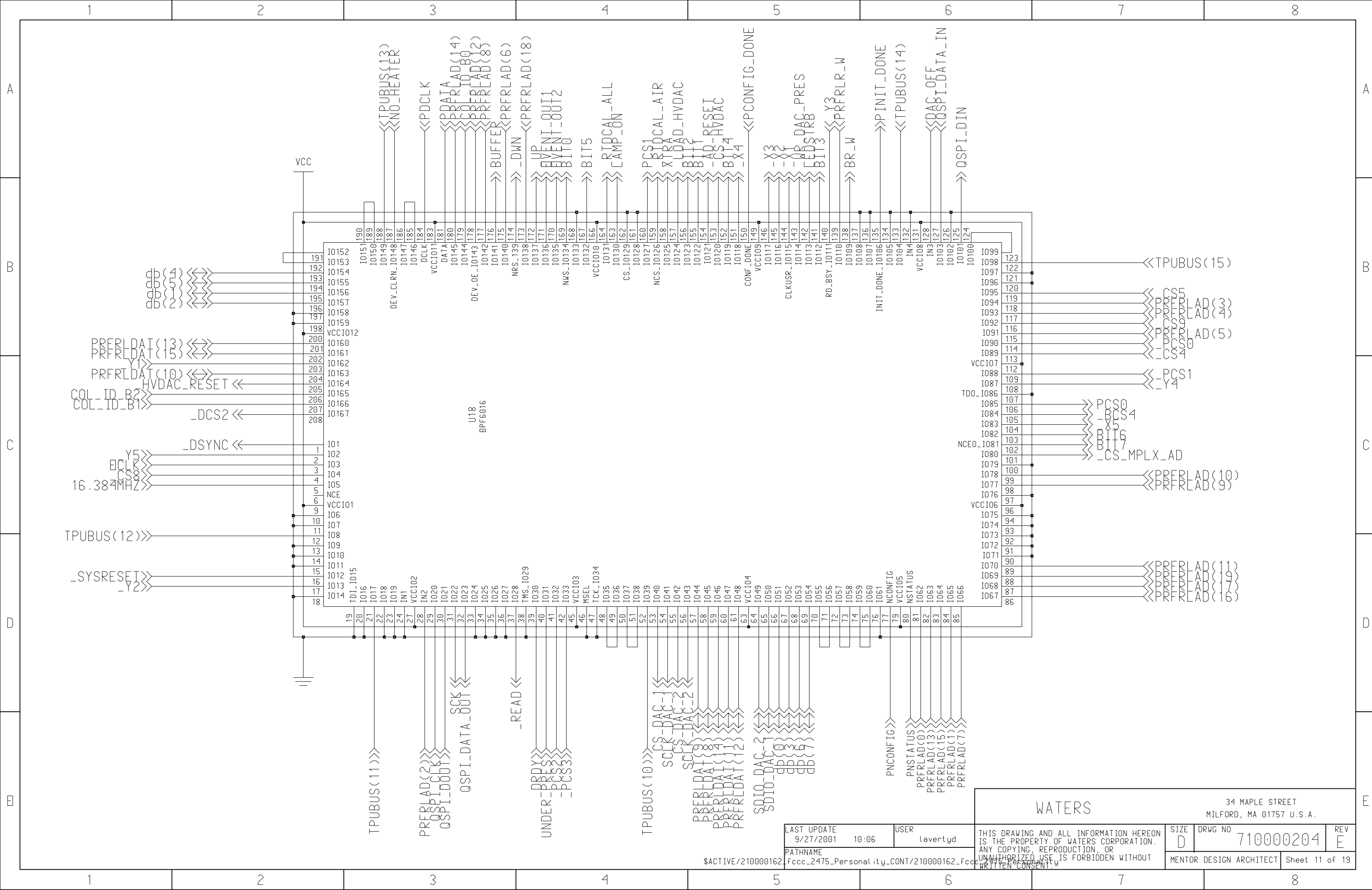
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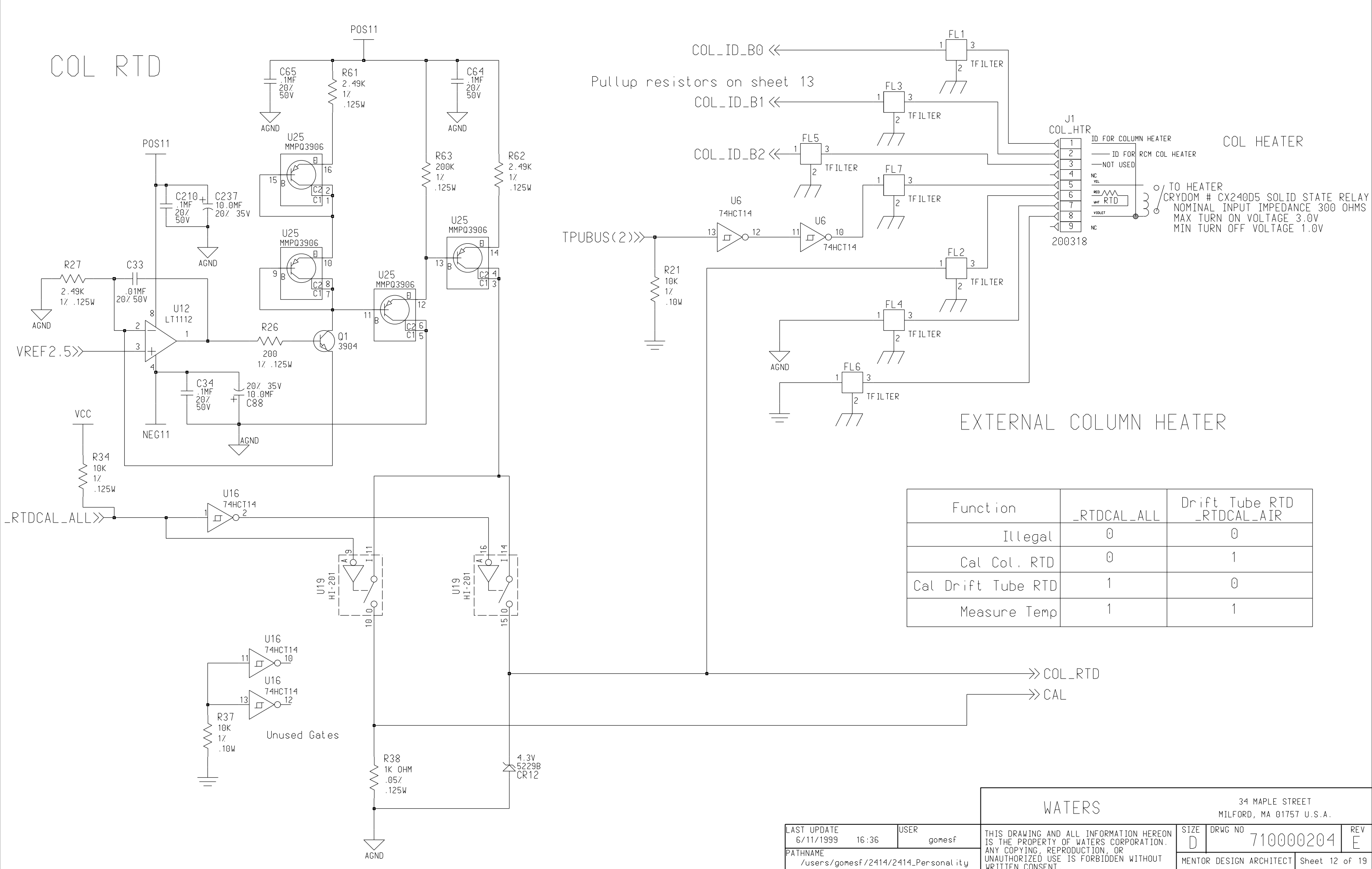
DRWG NO
710000204

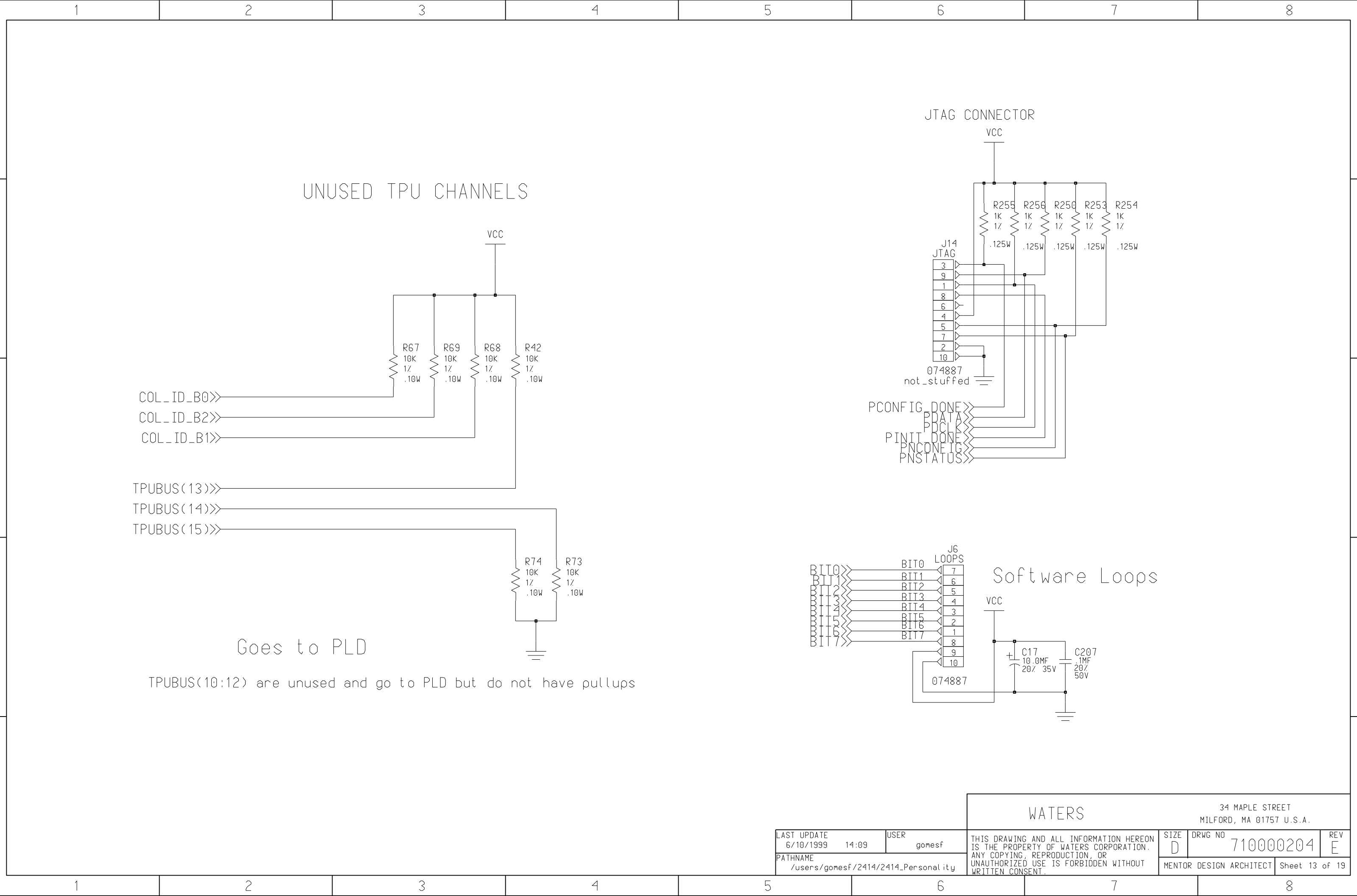
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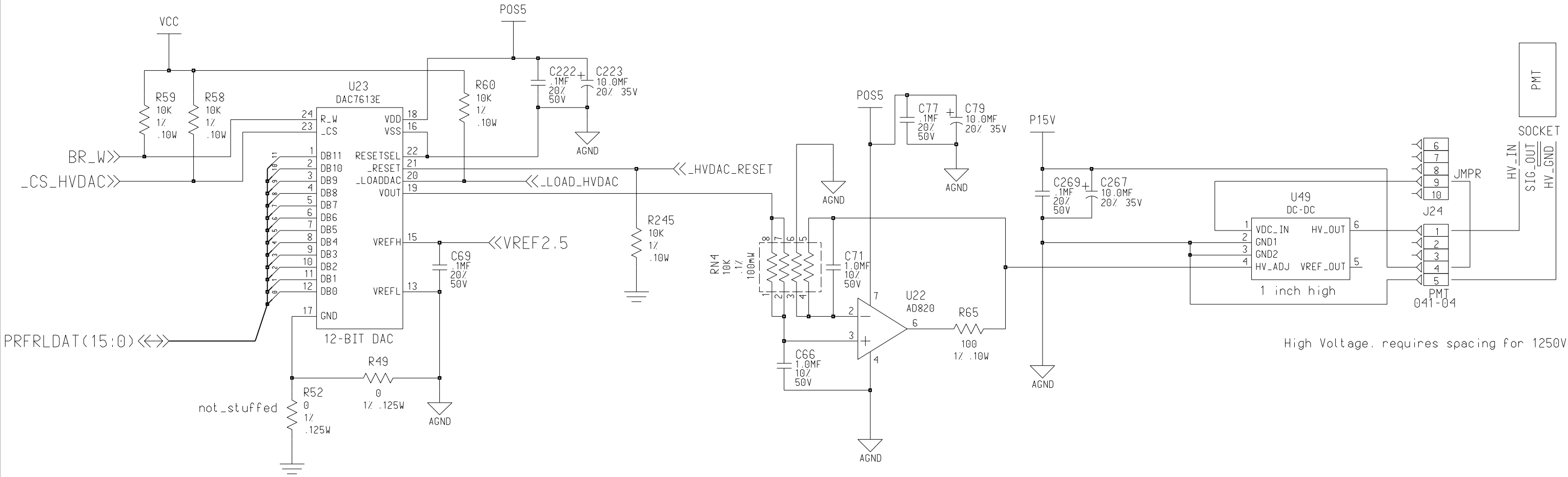
MENTOR DESIGN ARCHITECT

Sheet 10 of 19







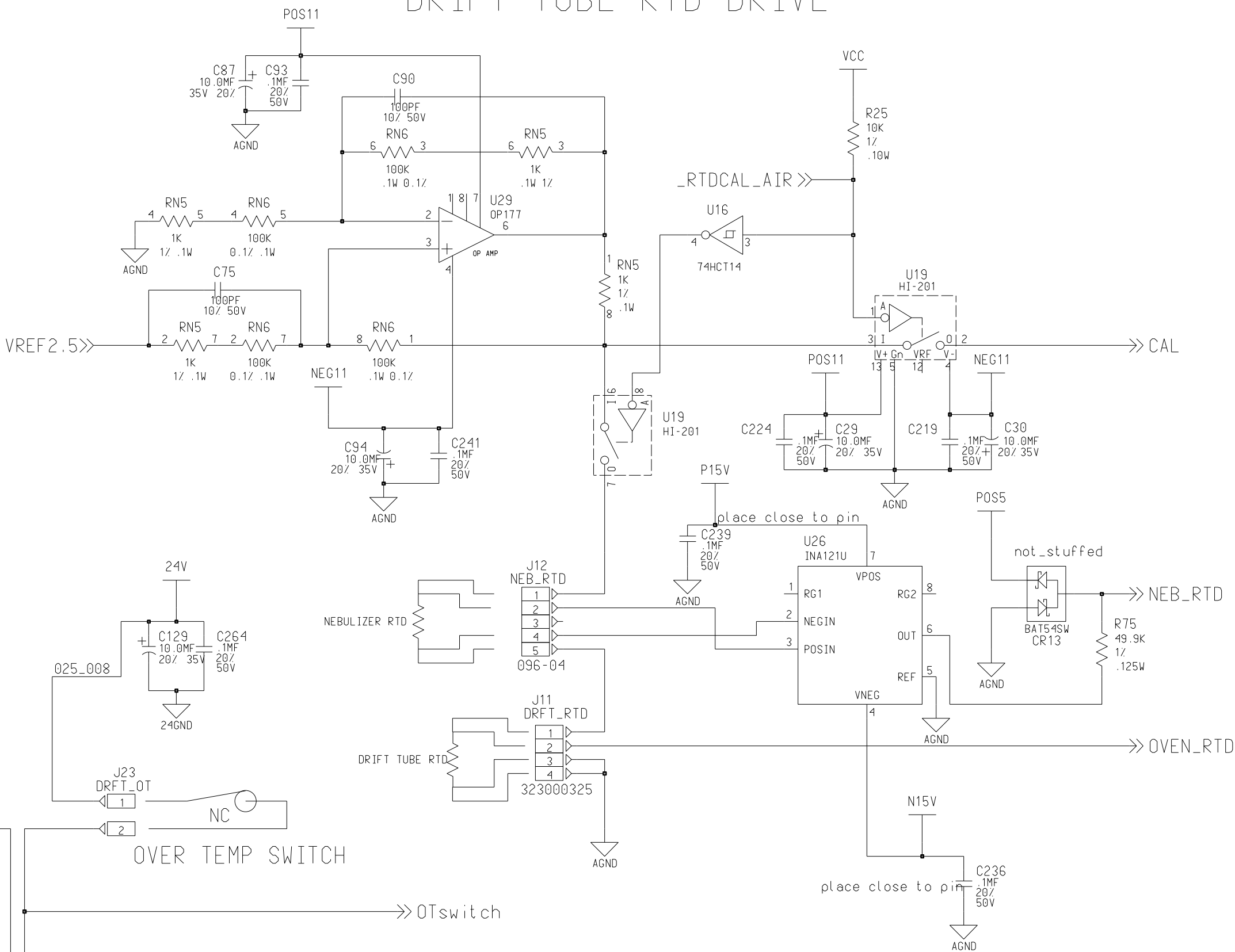
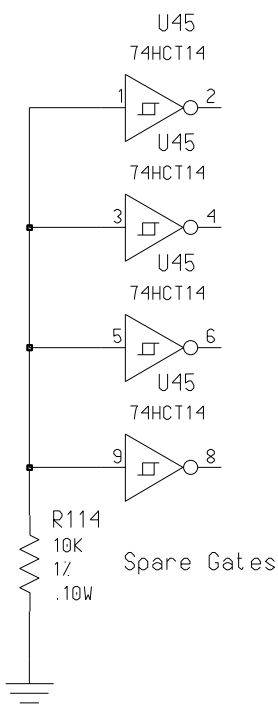


High Voltage Control for PMT

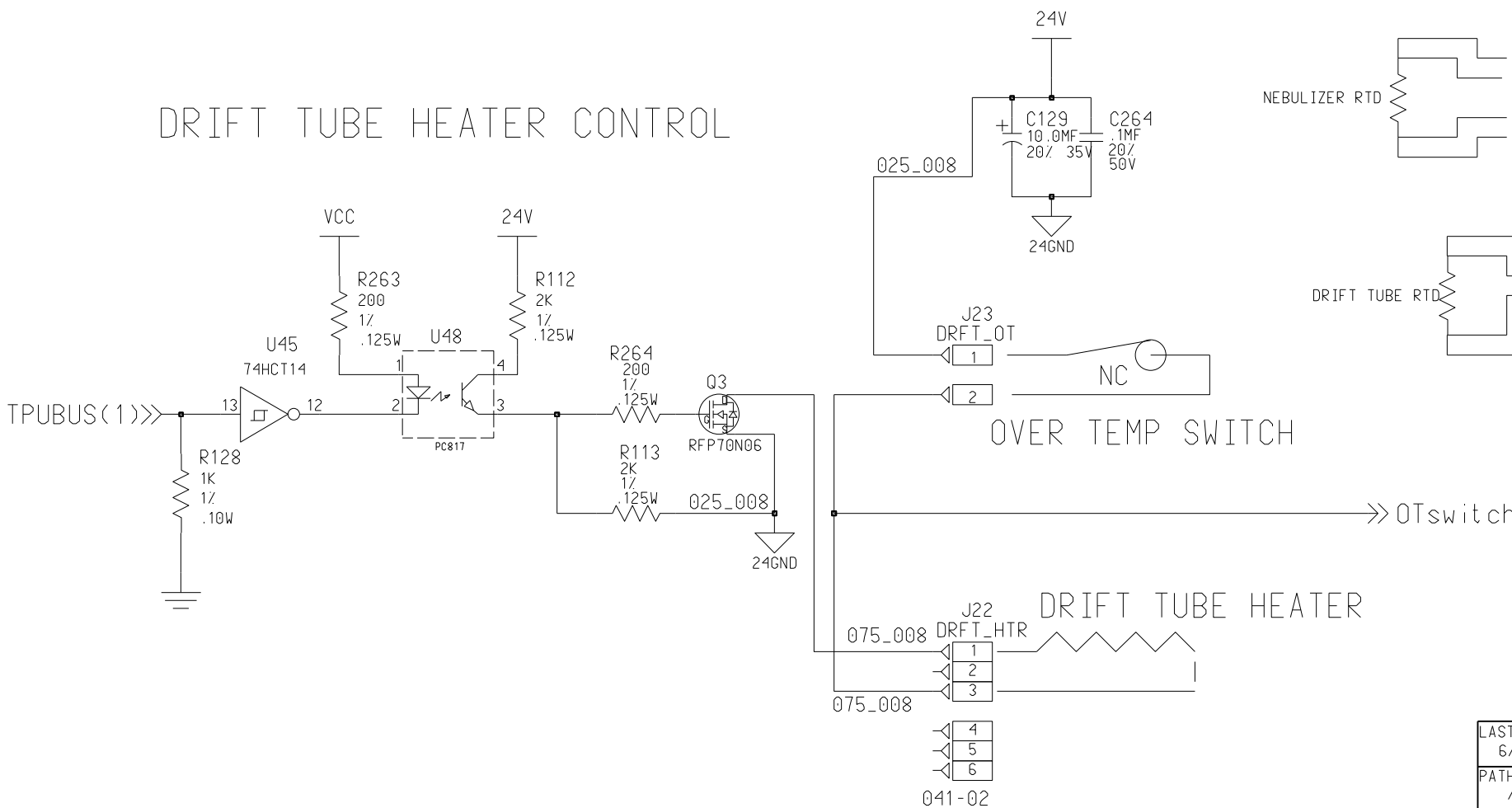
NOTES:
1. UNLESS OTHERWISE SPECIFIED RESISTOR VALUES ARE EXPRESSED IN OHMS.

		DESIGN J. ENGINR 12/12/99	WATERS 34 MAPLE ST. MILFORD, MA 01757 U.S.A.		
		DRAWN J. DRAFTR 12/12/99			
LAST UPDATE 12/4/2001 14:09	USER shaop	CHECKED	TITLE SCHEM, PCB 2420 SCHEMATIC		
PATHNAME /users/shaop/work/24xx-ELSD_Detector_Personality		ENGINEER			
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		APPD	MENTOR DESIGN ARCHITECT		Sheet 14 of 19

DRIFT TUBE RTD DRIVE



DRIFT TUBE HEATER CONTROL



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6/11/1999 16:3

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SIZE D	DRWG NO 7100000204	REV E
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MENTOR DESIGN ARCHITECT Sheet 15 of 19

Thermistor



Exhaust Heater Control



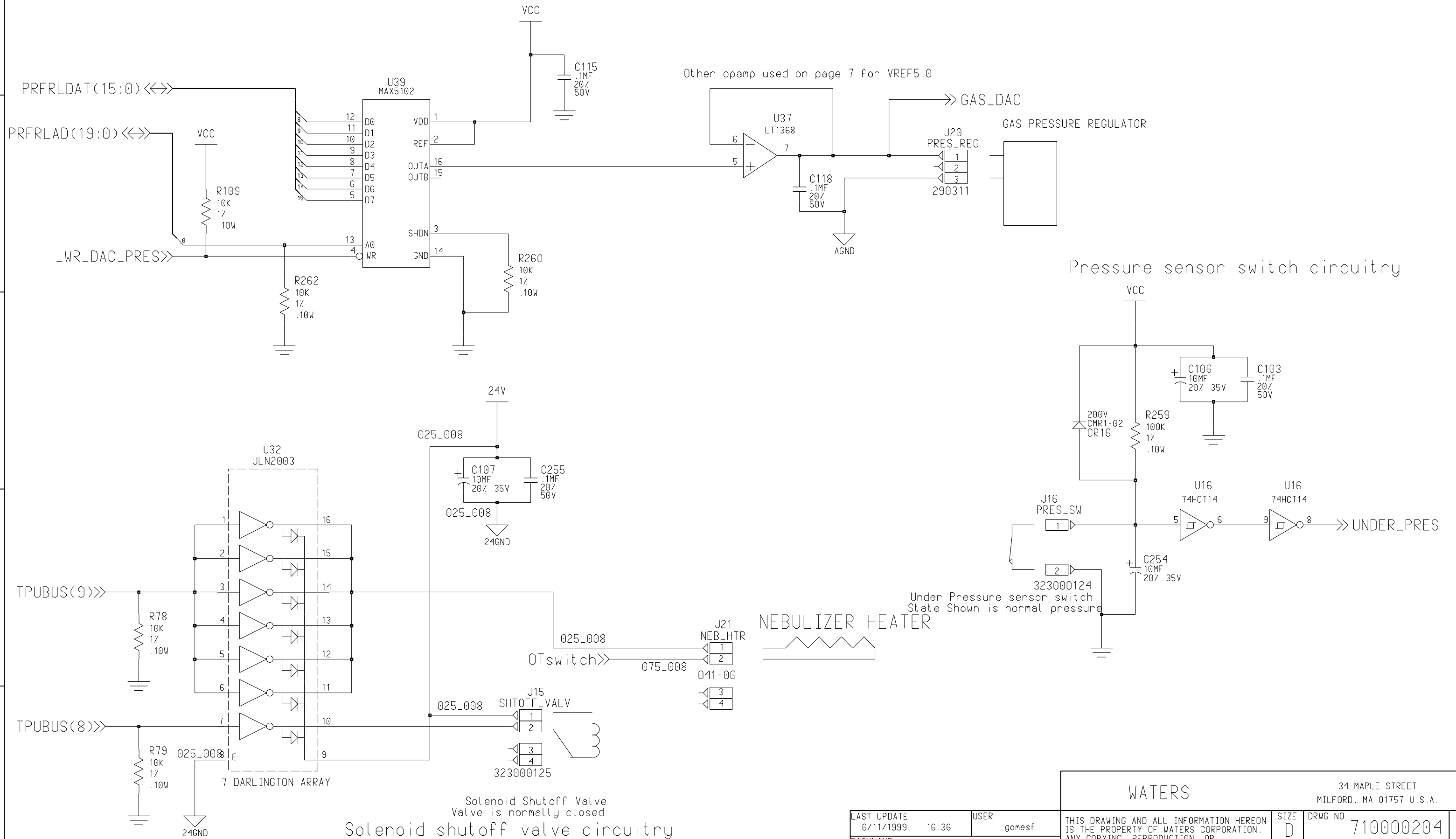
OPTICS HEATER CONTROL

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PATHNAME /users/gomesf/2414/2414_Personality			MENTOR DESIGN ARCHITECT		Sheet 16 of 19

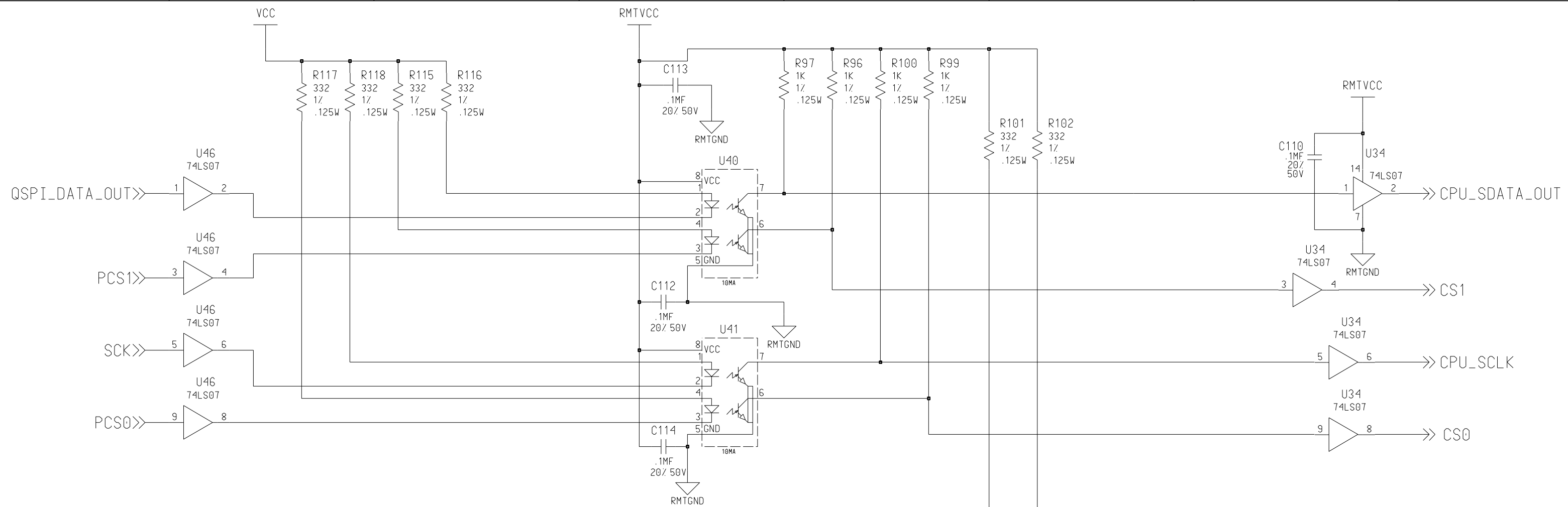
NEBULIZER GAS CONTROL



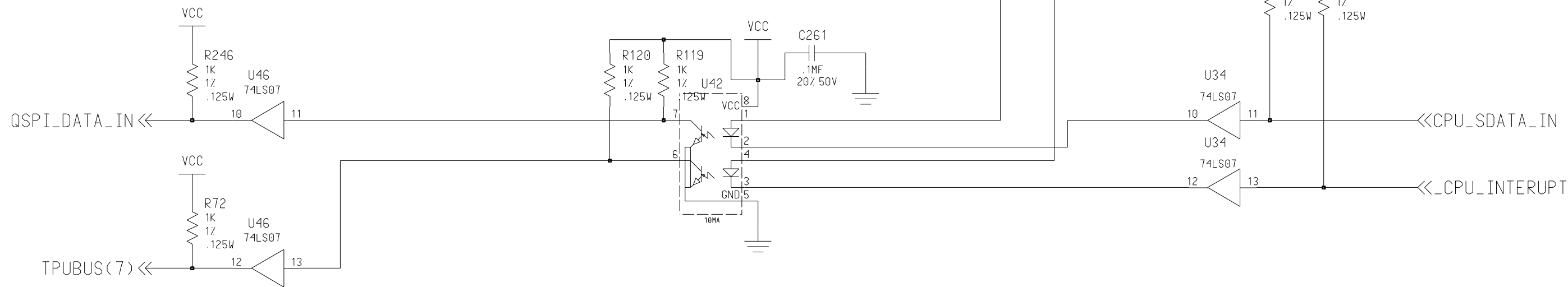
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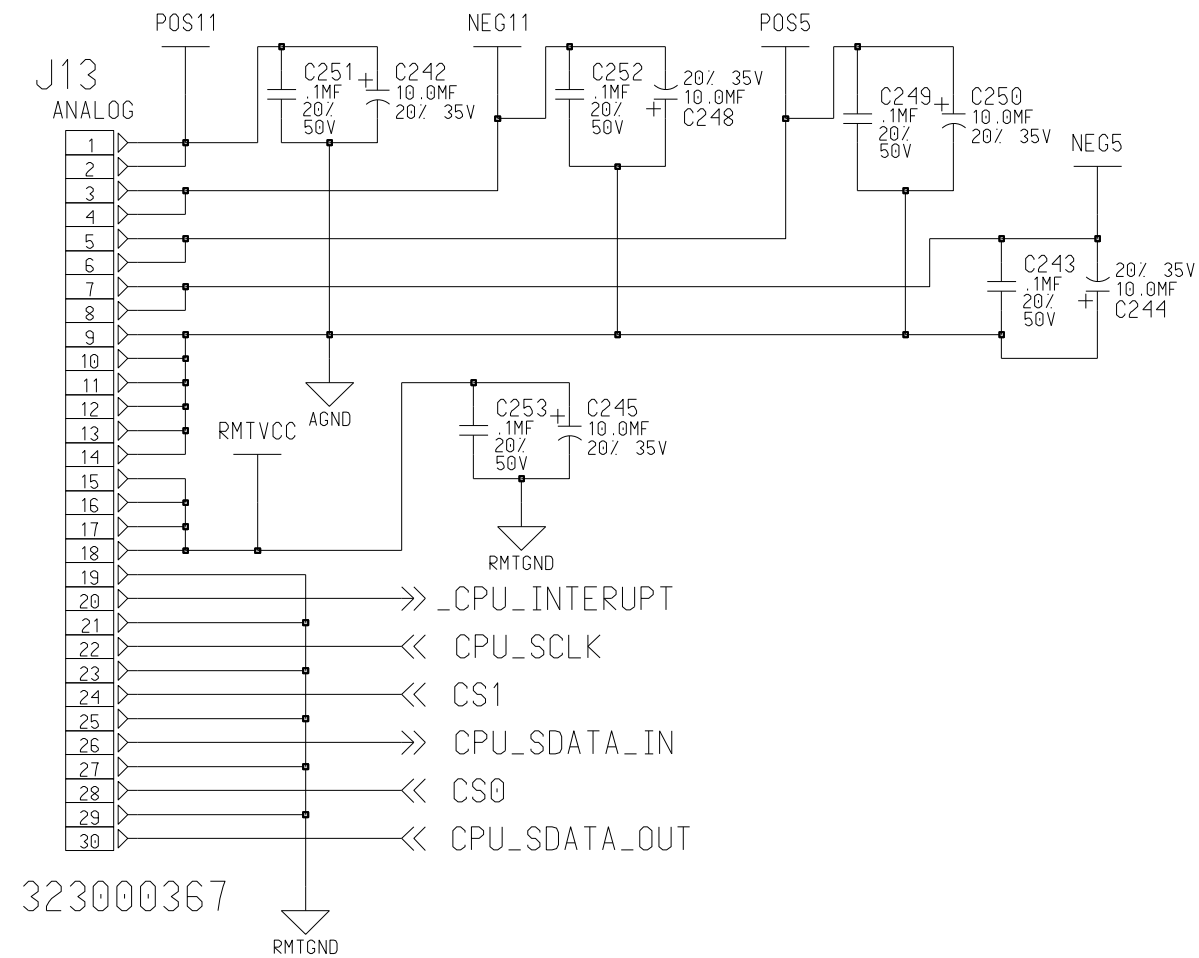
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PATHNAME /users/gomesf/2414/2414_Personal ity			MENTOR DESIGN ARCHITECT		Sheet 17 of 19



Front End Analog Board Opto's





T0/From Front End Analog PCB

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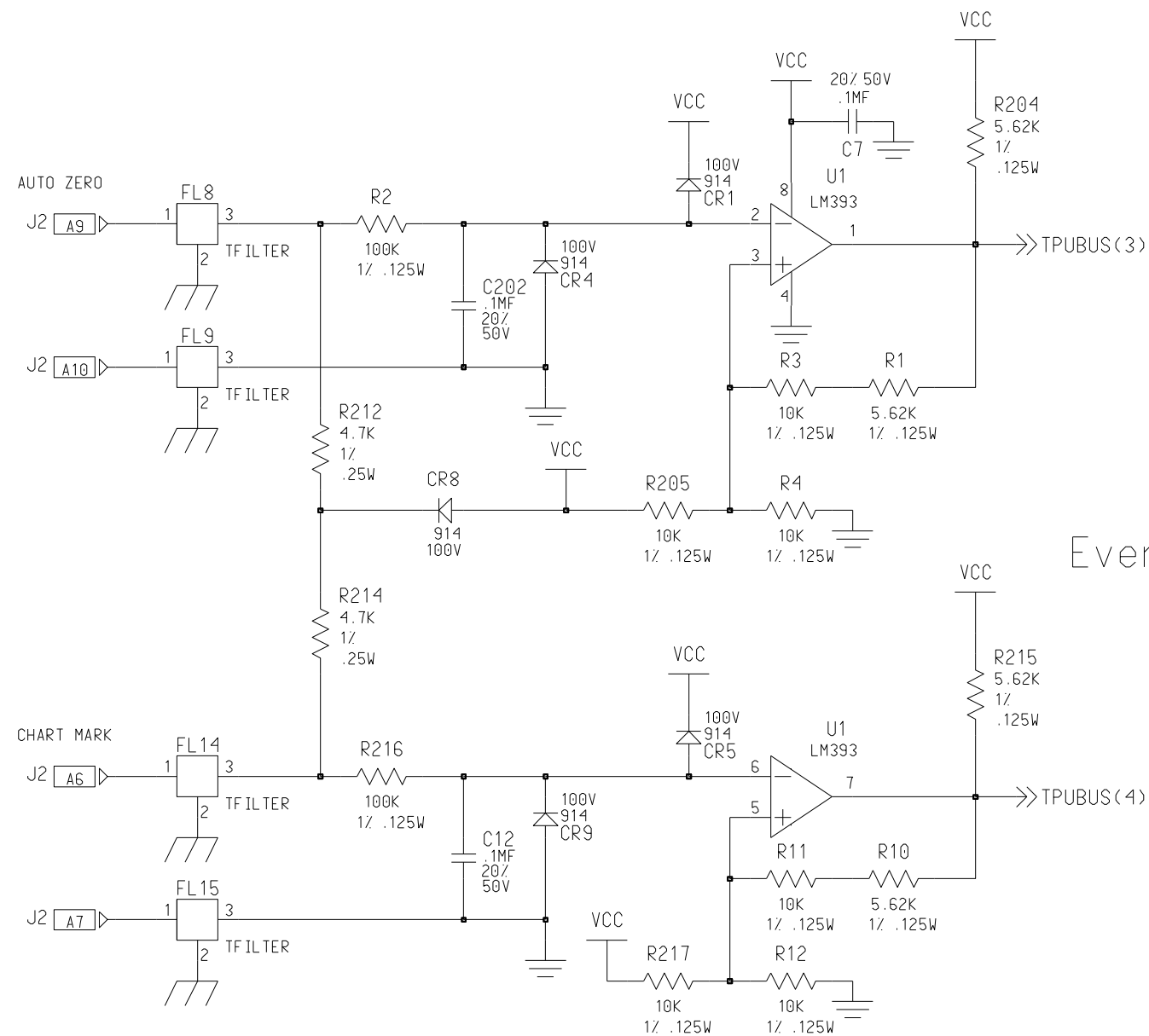
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DRWG NO
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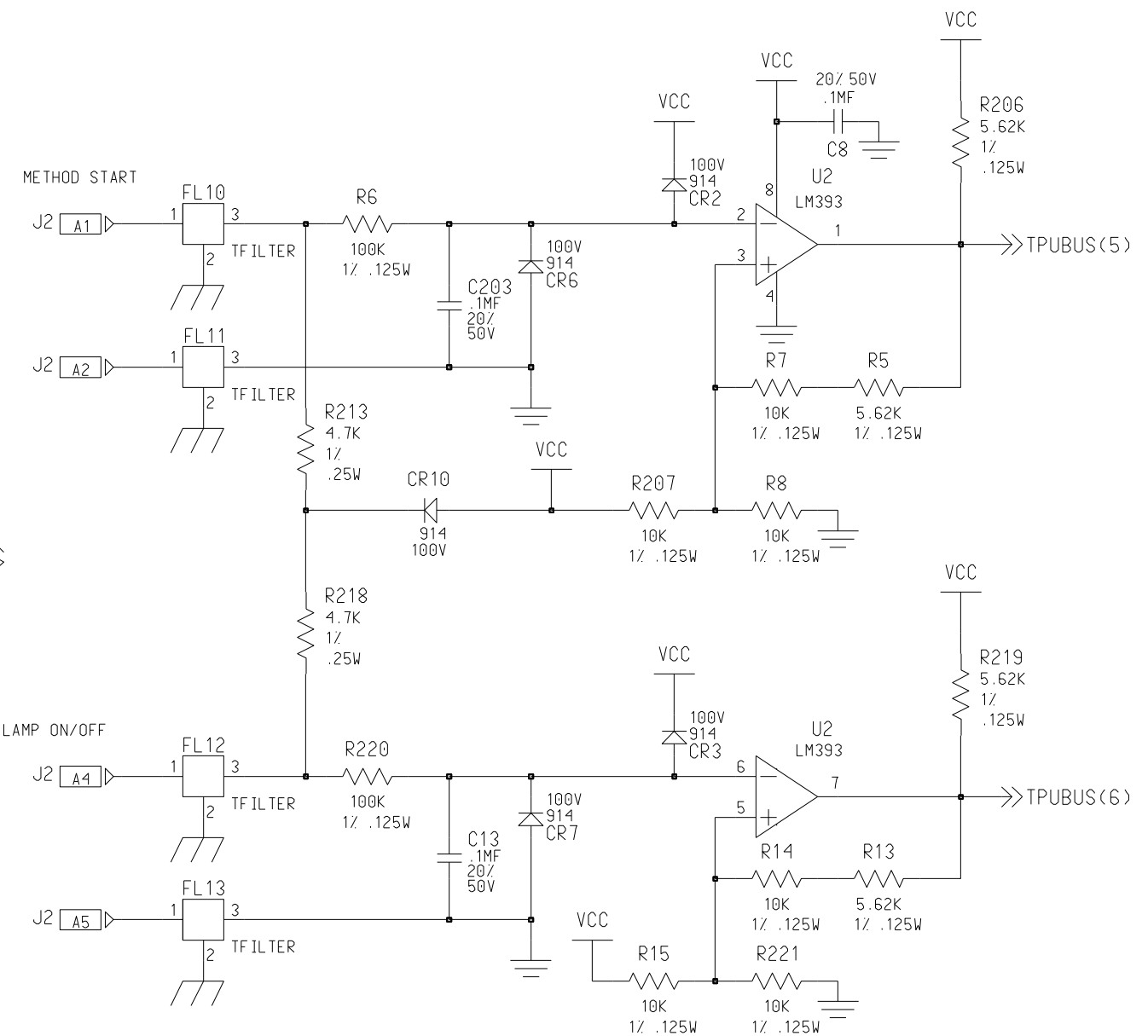
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MENTOR DESIGN ARCHITECT

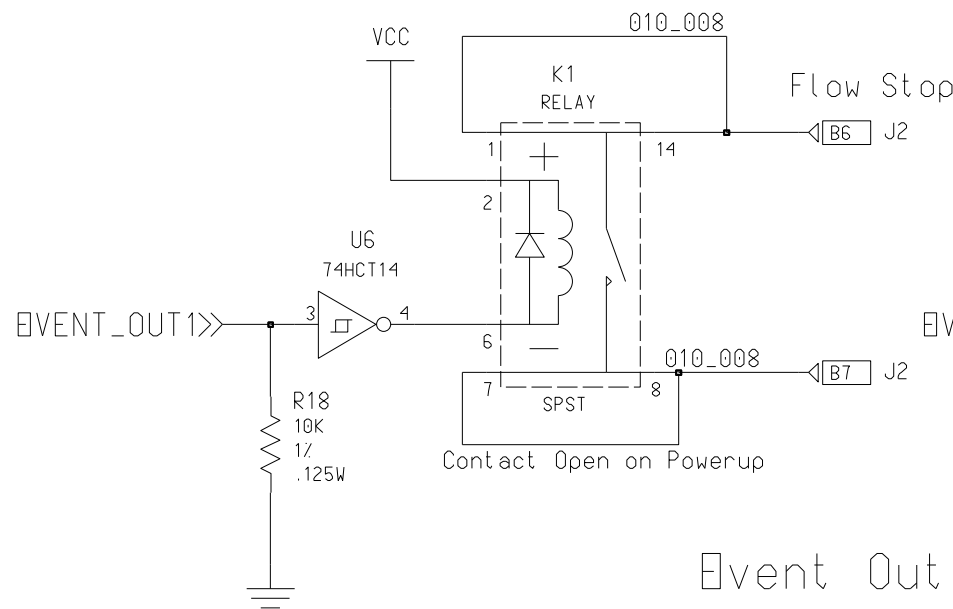
Sheet 19 of 19



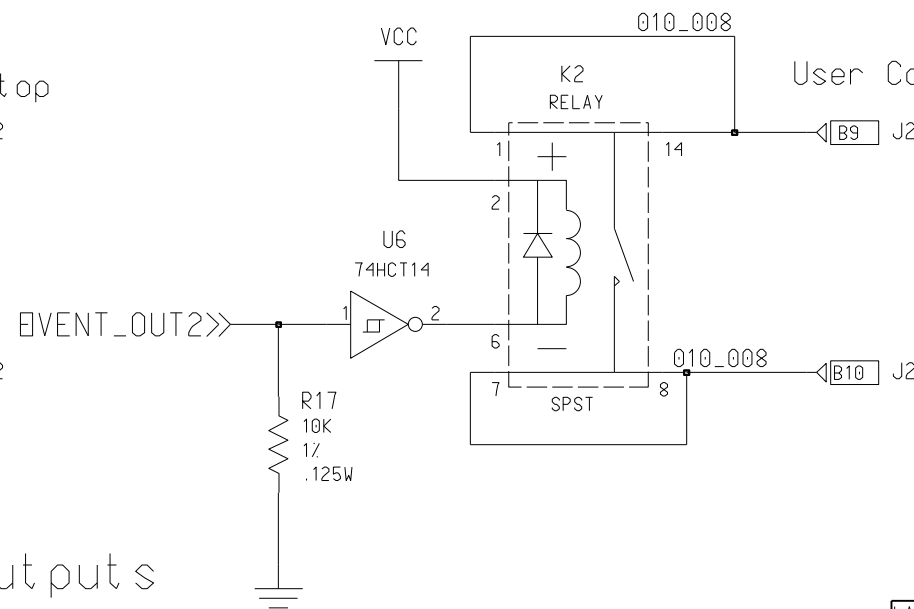
Event Inputs



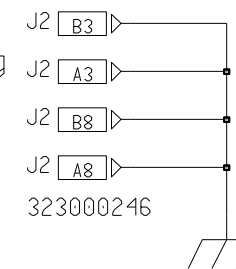
V+ Threshold is 3.0 volts and V- is 2.0 volts



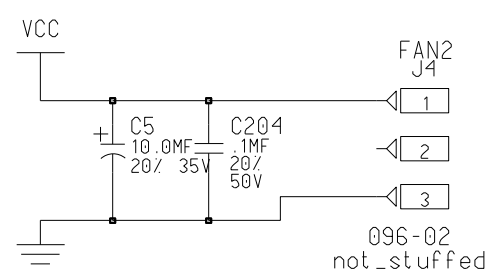
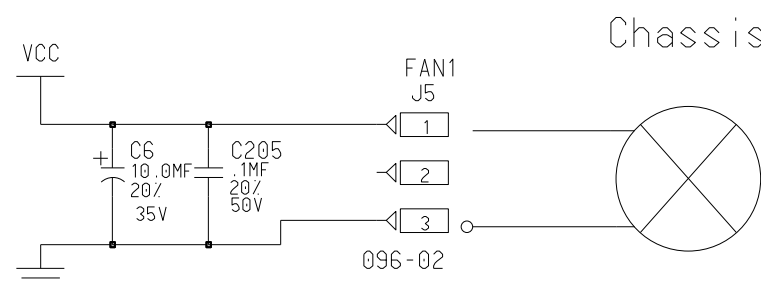
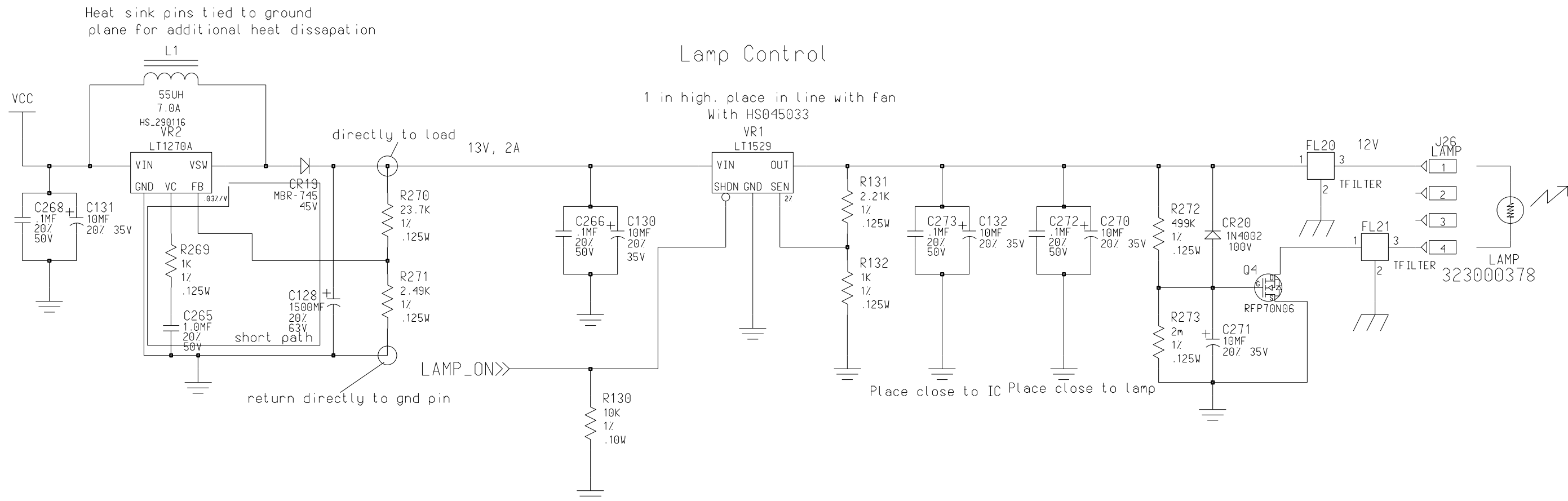
Event Outputs



User Config



J20	
B	A
1	ANALOG OUT 1
2	GROUND
3	GROUND
4	ANALOG OUT 2
5	GROUND
6	EVENT_OUT1
7	CHART MARK
8	GROUND
9	EVENT_OUT2
10	AUTO ZERO



5V Fans to help with minimum load of 3A for power supply

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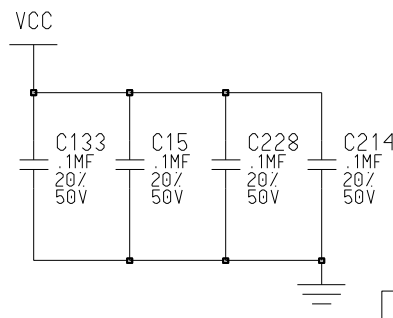
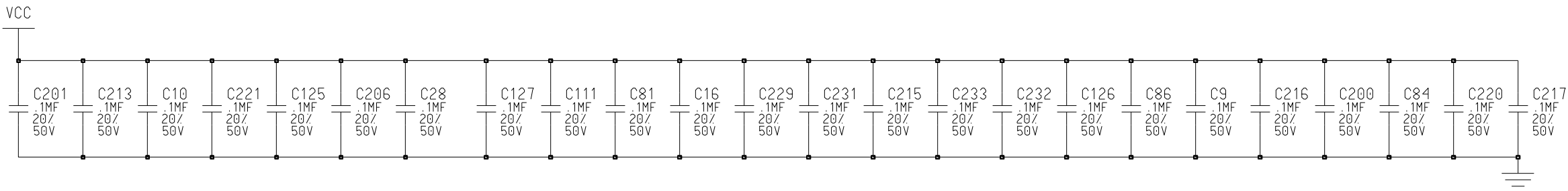
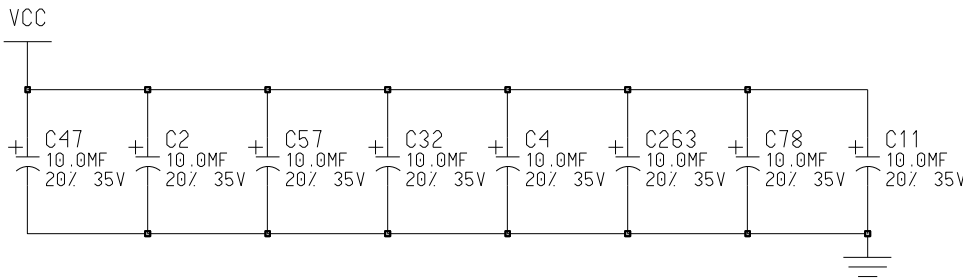
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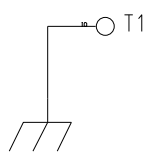
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MENTOR DESIGN ARCHITECT

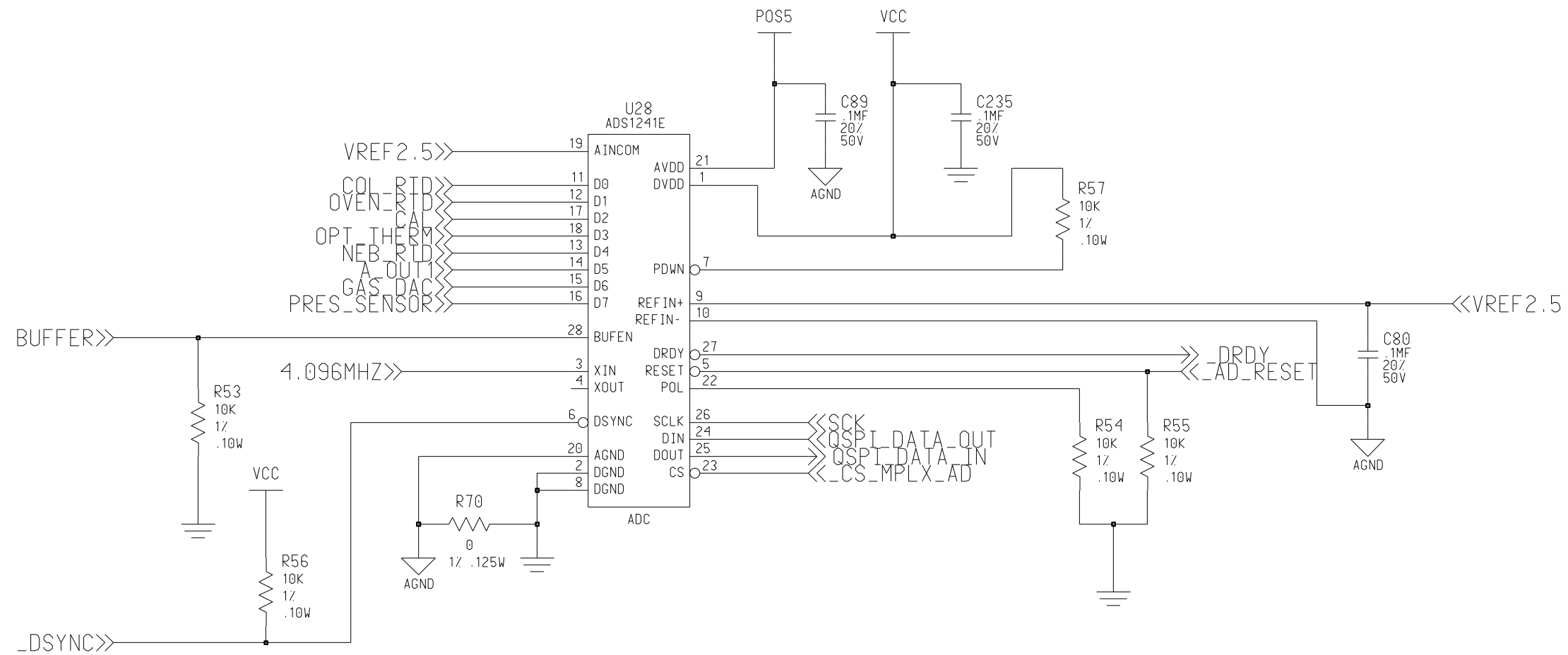
Sheet 4 of 19



De-Coupling Capacitors



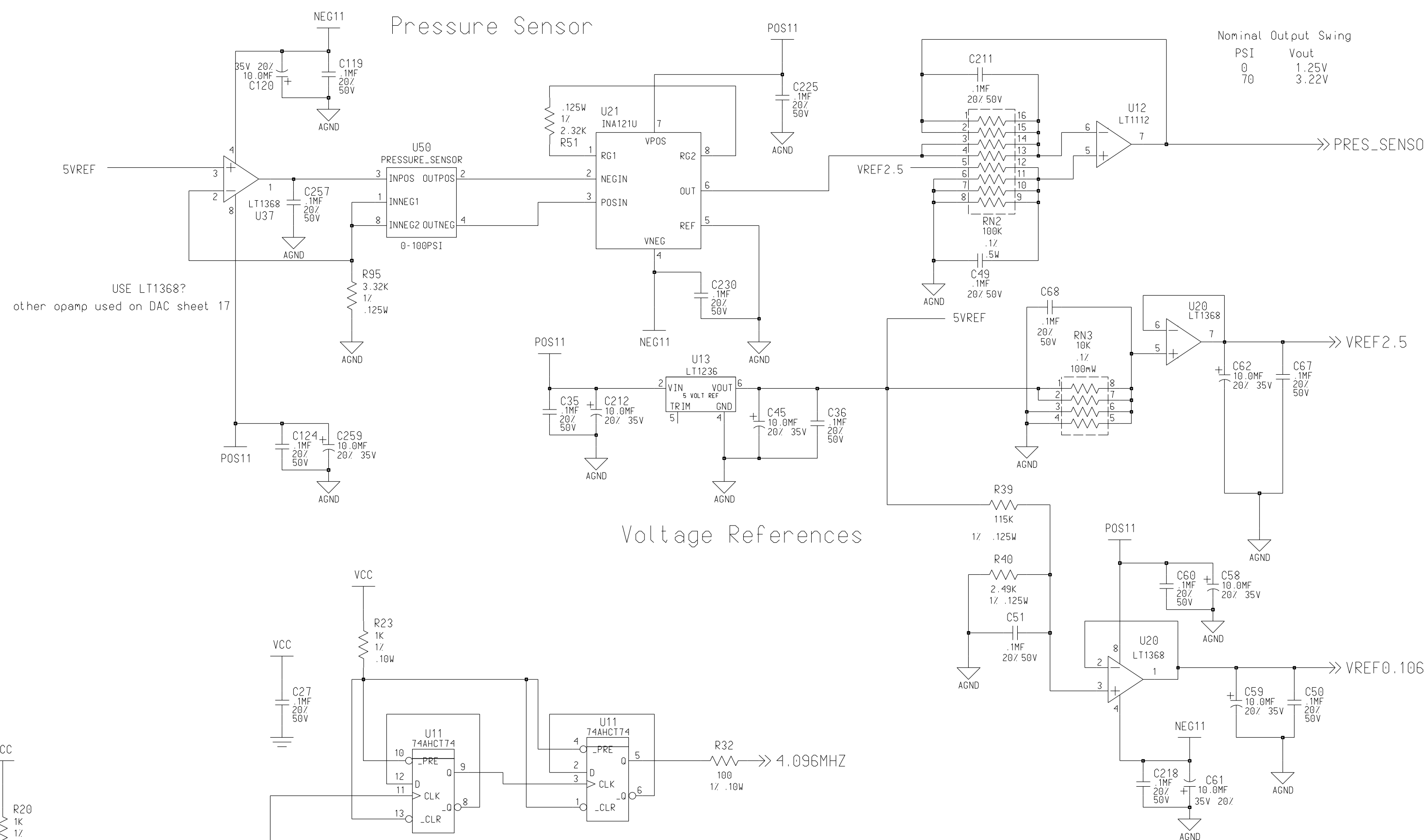
Multiplexed A/D for Temp Sensors



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PATHNAME \$ACTIVE/210000162-bbbb-2475_Personal.ity			MENTOR DESIGN ARCHITECT		Sheet 6 of 19



USE LT1368?
other opamp used on DAC sheet 17

Nominal Output Swing	
PSI	Vout
0	1.25V
70	3.22V

this clock added for multiplex adc

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